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**IN THE SPECIFICATION:** 

Please amend the specification by replacing the previously presented abstract

with the following abstract:

-- A clock and data recovery unit for recovering a received serial data bit

stream having: phase adjustment means for adjustment of a sampling time in the

center of a unit interval of the received data bit stream, wherein the phase adjustment

means comprises means for generating equidistant reference phase signals, a phase

interpolation unit, an oversampling unit, a serial-to-parallel-conversion unit, a binary

phase detection unit, and a loop filter; and data recognition means for recovery of the

received data stream which includes a number of parallel data recognition FIR-

Filters, wherein each data recognition FIR-Filter comprises a weighting unit, a

summing unit, and a comparator unit. --